## TRANSMITTAL

## Flectronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	ELECTRICALLY TUNABLE ON-CHIP RESISTOR
Application Number :	
Date :	
First Named Applican	t: James W. Adkisson
Confirmation Number	
Attorney Docket Num	ber: BUR920020105US1

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Submitted By:	Elec. Sign.	Sign. Capacity
Anthony J. Canale	Anthony J. Canale	Agent
Registered Number: 51.526	/s/	

Documents being submitted:	Files
us-ids	BUR920020105US1-usidst.xml
	us-ids.dtd
	us-ids.xsl
us-request	BUR920020105US1-usrequ.xml
	us-request.dtd
	us-request.xsl
us-assignment	BUR920020105US1-usassn.xml
	us-assignment.xsl
	us-assignment.dtd
	bur920020105assign1.tif
	bur920020105assign2.tif
us-fee-sheet	BUR920020105US1-usfees.xml
	us-fee-sheet.xsl
	us-fee-sheet.dtd
application-body	BUR920020105US1spec-trans.xml
	us-application-body.xsl
	application-body.dtd
	wipo.ent
	mathml2.dtd
	mathml2-qname-1.mod
	isoamsa.ent
	isoamsb.ent
	isoamsc.ent
	isoamsn.ent
	isoamso.ent
	isoamsr.ent
	isogrk3.ent
	isomfrk.ent
	isomopf.ent
	isomscr.ent
	isotech.ent
	isobox.ent
	isocyr1.ent
	isocyr2.ent
	isodia.ent
	isolat1.ent
	isolat2.ent
	isonum.ent
	isopub.ent
	mmlextra.ent
	mmlalias.ent
	soextblx.dtd
	BUR920020105US1FIGSsheet1of10.tif
	BUR920020105US1FIGSsheet2of10.tif BUR920020105US1FIGSsheet3of10.tif
	BUR920020105US1FIGSsneet30f10.tif BUR920020105US1FIGSsheet40f10.tif
	BUR920020105US1FIGSsneet40f10.tif BUR920020105US1FIGSsheet5of10.tif
	BUR920020105US1FIGSsneet50f10.tif
	BUR920020105US1FIGSsheet7of10.tif
_	DU1920020103031FIG381Bet(0110.tll

Comments	
us-declaration	bur920020105dec2.tif
us-declaration	bur920020105dec1.tif
	BUR920020105US1FIGSsheet10of10.tif
	BUR920020105US1FIGSsheet9of10.tif
	BUR920020105US1FIGSsheet8of10.tif